

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :
Tsukasa SHIRAISHI et al. :
Serial No. NEW : **Attn: APPLICATION BRANCH**
Filed May 20, 2004 : Attorney Docket No. 2004-0787

SEMICONDUCTOR DEVICE INCLUDING A
WIRING BOARD WITH A THREE-DIMENSIONAL
WIRING PATTERN
(Rule 1.53(b) Divisional of Serial No. 10/337,879,
Filed January 8, 2003)

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY
FEES FOR THIS PAPER TO DEPOSIT
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CLAIM OF PRIORITY UNDER 35 USC 119

Commissioner for Patents
P.O. Box 1450
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Sir:

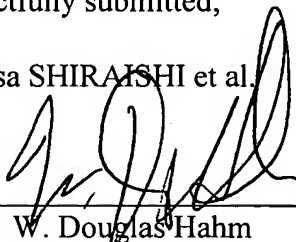
Applicants in the above-entitled application hereby claim the date of priority under the International Convention of Japanese Patent Application No. P09-250304, filed September 16, 1997, filed as acknowledged in the Declaration of this application.

A certified copy of said Japanese Patent Application is of record in grandparent application Serial No. 09/725,283, filed November 29, 2000.

Respectfully submitted,

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